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Classical Ising model test for quantum circuits

Joseph Geraci^{1,2,4} and Daniel A Lidar^{3,5}

¹ Department of Mathematics, University of Toronto, Toronto, Ontario M5S 2E4, Canada

² Center for Quantum Information Science and Technology, University of Southern California, Los Angeles, CA 90089, USA

³ Departments of Chemistry, Electrical Engineering, and Physics, Center for Quantum Information Science and Technology, University of Southern California, Los Angeles, CA 90089, USA
E-mail: geraci.joseph@gmail.com and lidar@usc.edu

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Abstract. We exploit a recently constructed mapping between quantum circuits and graphs in order to prove that circuits corresponding to certain planar graphs can be efficiently simulated classically. The proof uses an expression for the Ising model partition function in terms of quadratically signed weight enumerators (QWGTs), which are polynomials that arise naturally in an expansion of quantum circuits in terms of rotations involving Pauli matrices. We combine this expression with a known efficient classical algorithm for the Ising partition function of any planar graph in the absence of an external magnetic field, and the Robertson–Seymour theorem from graph theory. We give as an example a set of quantum circuits with a small number of non-nearest-neighbor gates which admit an efficient classical simulation.

⁴ Current address: The Ontario Cancer Biomarker Network, MaRS-TMDT, Toronto, Ontario M5G 1L7, Canada.

⁵ Author to whom any correspondence should be addressed.

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1. Introduction

From its early days, quantum computing was perceived as a means to efficiently simulate physics problems [1, 2], and a host of results have been derived along these lines for quantum [3]–[15] and classical systems [16]–[27]. A natural problem relating quantum computation and statistical mechanics is to understand for which instances quantum computers provide a speedup over their classical counterparts for the evaluation of partition functions [16, 22]. For the *Potts model*, results obtained in [23] provide insight into this

problem when the evaluation is an additive approximation. We provided a class of examples for which there is a quantum speedup when one seeks an exact evaluation of the Potts partition function [24].

In this work, we address the connection between quantum computing and classical statistical mechanics from the opposite perspective. Namely, we seek to find restrictions on the power of quantum computing, by employing known results about efficiently simulatable problems in statistical mechanics. Specifically, we restrict our attention to the *Ising model* partition function Z , and use a mapping between graph instances of the Ising model and quantum circuits introduced in [28], to identify a certain class of quantum circuits that have an efficient classical simulation.

Restricted classes of quantum circuits, which can be efficiently simulated, have classically been known since the Gottesman–Knill theorem [29]. This theorem states that a quantum circuit using only the following elements can be simulated efficiently on a classical computer: (i) preparation of qubits in computational basis states, (ii) quantum gates from the Clifford group (Hadamard, controlled-NOT gates and Pauli gates) and (iii) measurements in the computational basis. Such ‘stabilizer circuits’ on n qubits can be simulated in $O(n \log n)$ time using the graph state formalism [30]. Other early results include [31], where the notion of matchgates was introduced and the problem of efficiently simulating a certain class of quantum circuits was reduced to the problem of evaluating the Pfaffian. This was subsequently shown to correspond to a physical model of non-interacting fermions in one dimension, and extended to non-interacting fermions with arbitrary pairwise interactions [32]–[34] (see further generalizations in [35, 36]), and Lie-algebraic generalized mean-field Hamiltonians [37]. Criteria for efficient classical simulation of quantum computation can also be given in terms of upper bounds on the amount of entanglement generated in the course of the quantum evolution [38].

A result that is more directly related to the one we shall present in this work is given in [25], but within the measurement-based quantum computation (MQC) paradigm. MQC relies on the preparation of a multi-qubit entangled resource state known as the cluster state. It is known that MQC with access to cluster states is universal for quantum computation. Bravyi and Raussendorf [25] consider planar code states that are closely related to cluster states, in that a sequence of Pauli-measurements applied to the two-dimensional (2D) cluster state can result in a planar code state. MQC with planar code states consists of a sequence of measurements $\{M_1, M_2, \dots, M_n, M\}$, where the M_i are one-qubit measurements and M is a final measurement done on the remaining qubits in some basis which depends on the results of the M_i . Reference [25] demonstrates that planar code states are not a sufficient resource for universal quantum computation (and can be classically simulated). This fact is attributed to the exact solvability of the Ising partition function on planar graphs. Our results complement the work in [25], as they are provided in terms of the circuit model, and generalize to Ising model instances that correspond to graphs which are not necessarily subgraphs of a 2D grid.

Other conceptually related work uses the connection between graphs and quantum circuits and the formalism of tensor network contractions to show that any polynomial-sized quantum circuit of one- and two-qubit gates, which has log depth and in which the two-qubit gates are restricted to act at bounded range, may be classically efficiently simulated [36, 39, 40]. A tensor network is a product of tensors associated with vertices of some graph G such that every edge of G represents a summation (contraction) over a matching pair of indexes. We also use a relationship between quantum circuits and graphs but whose construction is quite different [28]. Also, Bravyi [41] connects matchgates and tensor network contractions to notions of efficient simulation.

Finally, other closely related work was recently reported by Van den Nest *et al* in [42] (see also [43]–[45]), which addresses the classical simulatability of quantum circuits. Their results use a connection to the partition function of spin models, as do we, and they too provide a mapping between classical spin models and quantum circuits. Specifically pertinent to our work is the fact that they give criteria for the simulatability of quantum circuits, using the 2D Ising model. That is, circuits consisting of single-qubit gates of the form $e^{i\theta\sigma_x}$ and nearest-neighbor gates of the form $e^{i\phi\sigma_z\otimes\sigma_z}$ are classically efficiently simulatable. We shall discuss how the nearest-neighbor restrictions can be lifted while retaining efficient classical simulatability.

The structure of this paper is as follows. We begin with a brief review of the Ising model in section 2, where we define the Ising partition function Z . In section 3 we review quadratically signed weight enumerators (QWGTs) and their relationship to quantum circuits, and review the relationship between QWGTs and Z . In section 4 we introduce an ansatz that allows one to associate graph instances of the Ising model with circuit instances of the quantum circuit model. In this section we derive a key result: an explicit connection between the partition function for the Ising model on a graph, and a matrix element of the unitary representing a quantum circuit which is related to this graph via the graph's incidence matrix (equation (35)). We then present our main result in section 5: a theorem on efficiently simulatable quantum circuits. The proof depends on the fact that there are algorithms for the efficient evaluation of Z for planar instances of the Ising model. We also discuss the relation to previous work. In section 7 we present a discussion and some suggestions for future work, including the possibility of a quantum algorithm for the additive approximation of Z . We conclude in section 8. The appendix gives a review of pertinent concepts from graph theory, and additional details, including some proofs.

2. Ising spin model

We briefly introduce the Ising spin model accompanied by some notation and definitions. Let $G = (E, V)$ be a finite, arbitrary undirected graph with $|E|$ edges and $|V|$ vertices. In the Ising model, each vertex i is occupied by a classical spin $\sigma_i = \pm 1$, and each edge $(i, j) \in E$ represents a bond J_{ij} (interaction energy between spins i and j).

Definition 1. *An instance of the Ising problem is the data $\Delta \equiv (G, \{J_{ij}\})$, i.e., Δ represents a weighted graph.*

The Hamiltonian of the spin system is

$$H_{\Delta}(\sigma) = - \sum_{(i,j) \in E} J_{ij} \sigma_i \sigma_j. \quad (1)$$

A spin configuration $\sigma = \{\sigma_i\}_{i=1}^{|V|}$ is a particular assignment of spin values for all $|V|$ spins. A bond with $J_{ij} > 0$ is called ferromagnetic, and a bond with $J_{ij} < 0$ is called antiferromagnetic. The probability of the spin configuration σ in thermal equilibrium for a system in contact with a heat reservoir at temperature T is given by the Gibbs distribution, $P_{\Delta}(\sigma) = (1/Z_{\Delta})W_{\Delta}(\sigma)$, where the Boltzmann weight is $W_{\Delta}(\sigma) = \exp[-\beta H_{\Delta}(\sigma)]$, $\beta = 1/kT$ is the inverse temperature in energy units, k is the Boltzmann constant and Z_{Δ} is the partition function:

$$Z_{\Delta}(\beta) \equiv \sum_{\sigma} \exp[-\beta H_{\Delta}(\sigma)]. \quad (2)$$

(Unless there is a risk of confusion, we will from now on write Z in place of $Z_{\Delta}(\beta)$ in order to simplify our notation.) Computation of the partition function is the canonical problem of

statistical mechanics, since once Z is known one can compute all thermodynamic quantities, such as the magnetization and heat capacity, by taking derivatives of $F = -k \log Z$ (the free energy) with respect to appropriate thermodynamic variables [48].

In this work we restrict our attention to the case $J_{ij} \in \{-J, 0, J\}$, with $J > 0$, which already gives rise to the full complexity of spin glass models and the associated computational hardness [46]. For example, with the above restriction, the problem of computing the partition function in the 3D spin-glass is NP-hard [47].⁶

3. Quadratically signed weight enumerators (QWGTs) and their relation to the Ising partition function

QWGTs were introduced by Knill and Laflamme [49].

Definition 2. A QWGT is a bi-variate polynomial of the form

$$S(A, B, x, y) = \sum_{b \in \ker A} (-1)^{b^t B b} x^{|b|} y^{n-|b|}, \quad (3)$$

where A and B are $0,1$ -matrices with B of dimension $n \times n$ and A of dimension $m \times n$. The variable b in the summand ranges over $0, 1$ -column vectors of dimension n satisfying $Ab = 0$ (in the kernel, or nullspace of A), b^t is the transpose of b , and $|b|$ is the Hamming weight of b (the number of ones in the vector b). All calculations involving A , B or b are done modulo 2.

Note that the evaluation of a QWGT, given that x and y are natural numbers, is in general #P-hard, because it includes the evaluation of the weight enumerator polynomial of a classical linear code [50]. The complexity class #P is the set of counting problems associated with the decision problems in the class NP.

3.1. QWGTs from quantum circuits

We shall now review in some detail how QWGTs were arrived at in [49] by considering expansions of quantum circuits. Let Ω be a quantum circuit formed by a temporal ordering of N gates g_k , and let $U(\Omega) = \prod_{k=N}^1 g_k = g_N \cdots g_1$ be the corresponding unitary operator. Note that a universal gate set can be achieved by allowing arbitrary rotations about tensor products of Pauli operators, i.e., each of the N gates g_k can be represented as

$$e^{-i\sigma_b \theta/2} = \cos\left(\frac{\theta}{2}\right) I - i \sin\left(\frac{\theta}{2}\right) \sigma_b, \quad (4)$$

where

$$\sigma_b = \bigotimes_{i=1}^n \sigma_{b_i}^{(i)}, \quad (5)$$

with n being the number of qubits, such that the Pauli matrices are

$$\begin{aligned} \sigma_{00} = I &= \begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}, & \sigma_{01} = \sigma_X &= \begin{pmatrix} 0 & 1 \\ 1 & 0 \end{pmatrix}, \\ \sigma_{11} = \sigma_Y &= \begin{pmatrix} 0 & -i \\ i & 0 \end{pmatrix}, & \sigma_{10} = \sigma_Z &= \begin{pmatrix} 1 & 0 \\ 0 & -1 \end{pmatrix}. \end{aligned}$$

⁶ Informally, NP-hard problems are those to which any problem in NP can be reduced in polynomial time. The complexity class NP consists of those problems whose solution can be verified in polynomial time.

Here, $b_i \in \{00, 01, 10, 11\}$, $b = \{b_i\}_{i=1}^n$ is a binary vector whose length is $2n$ and the superscript (i) represents the qubit that is operated on by the corresponding Pauli matrix. A circuit constructed using gates of the form (4) may be approximated efficiently to accuracy $O(\epsilon/N)$ with $\text{polylog}(N/\epsilon)$ overhead using a standard gate set, such as controlled-NOT with single-qubit gates, and there is a classical algorithm that computes such approximations efficiently [49, 51]. A universal set of one- and two-qubit gates can be obtained from g_k s as in equation (4) from the rotations with $\cos(\theta/2) = 3/5$ (i.e., $\theta = 2 \arcsin(4/5)$) around operators of weight at most two (the weight of σ_b is the number of nonzero pairs of bits in b) (theorem 3.3, case (e), of [52]). Letting

$$\cos\left(\frac{\theta}{2}\right) = \frac{\alpha}{\gamma}, \quad \sin\left(\frac{\theta}{2}\right) = \frac{\alpha'}{\gamma}, \quad (6)$$

so that $\gamma = \sqrt{\alpha^2 + \alpha'^2}$, we rewrite equation (4) as

$$g_k = \frac{1}{\gamma} (\alpha I - i\alpha' \sigma_{b_k}). \quad (7)$$

The gate set is still universal if $U(\Omega)$ is expressed as a product of *real* gates [53], i.e., if each gate g_k contains an odd number of σ_Y s, so that $i\sigma_{b_k}$ in equation (7) is a real-valued matrix. Following [49], we adopt this convention, so that from now on b_k is a binary vector of length $2n$, subject to the restriction that the b_k can only contain an odd number of 11s. Moreover, the gate set is still universal if we assume that the orientation (the sign of θ) is positive if the number of σ_Y s is $1 \pmod{4}$ and negative otherwise [49]. This means that we can replace equation (7) by

$$g_k = \frac{1}{\gamma} (\alpha I \pm i\alpha' \sigma_{b_k}), \quad (8)$$

with the sign determined by the number of σ_Y s in σ_{b_k} . Then, by defining

$$\tilde{\sigma}_{b_k} = (-i)^{|b|_Y} \sigma_{b_k}, \quad (9)$$

where $|b|_Y$ is the (always odd) number of σ_Y s occurring in σ_{b_k} , we may write

$$g_k = \frac{1}{\gamma} (\alpha I + \alpha' \tilde{\sigma}_{b_k}), \quad (10)$$

which is the desired representation of real-valued gates [49].

Now define C to be the block diagonal matrix whose blocks consist of

$$c = \begin{pmatrix} 0 & 1 \\ 0 & 0 \end{pmatrix}, \quad (11)$$

i.e.

$$C = \bigoplus_{i=1}^n c. \quad (12)$$

Then the property that b has an odd number of 11s is given by $b^t C b = 1$. In addition, we have the multiplication rule

$$\tilde{\sigma}_{b_1} \tilde{\sigma}_{b_2} = (-1)^{b_1^t C b_2} \tilde{\sigma}_{b_1 \oplus b_2}, \quad (13)$$

where the addition in the subscript is bit by bit modulo 2.

Let H be the $(2n \times N)$ matrix whose columns are the b_k :

$$H = (b_1 b_2 \cdots b_N). \quad (14)$$

H is a linear size, bijective representation of the quantum circuit, where each column represents a gate and every pair of rows represents a qubit.

Definition 3. A matrix H which is constructed according to equation (14) is called the ‘ H -matrix representation’ of the quantum circuit Ω .

Using the rule (13), we then have the following expansion [49],

$$\begin{aligned} U(\Omega) &= \prod_{k=N}^1 g_k \\ &= \prod_{k=N}^1 \frac{1}{\gamma} (\alpha I + \alpha' \tilde{\sigma}_{b_k}) \\ &= \frac{1}{\gamma^N} \sum_a (-1)^{Q_{aa}} \alpha^{|a|} (\alpha')^{N-|a|} \tilde{\sigma}_{Ha}, \end{aligned} \quad (15)$$

where

$$Q_{aa'} \equiv a^t Q a' \quad (16)$$

and where the $N \times N$ lower-triangular matrix Q is defined by

$$Q \equiv \text{lwtr}(H^t C H), \quad (17)$$

and where a ranges over all binary column vectors of length N . (Thus $Q_{aa'}$ is not a matrix element of Q ; we use this notation merely for convenience.)

In order to make contact with the partition function of the Ising model, we shall be interested in the matrix element $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$, where $|\mathbf{0}\rangle = \otimes_{i=1}^n |0_i\rangle$, and $|0_i\rangle$ is the +1 eigenvector of $\sigma_Z^{(i)}$. For this matrix element to be nonzero, no qubit can be flipped, i.e., $U(\Omega)$ cannot contain any σ_X or σ_Y factors. When taking the same matrix element of the right-hand side of equation (15), we have $\langle \mathbf{0} | \tilde{\sigma}_{Ha} | \mathbf{0} \rangle$, and similarly, for this to be nonzero, $\tilde{\sigma}_{Ha}$ cannot have σ_X or σ_Y factors. This is enforced by summing only over those binary vectors a such that $CHa = 0$ [49]. Thus

$$\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle = \frac{1}{\gamma^N} \sum_{a \in \ker CH} (-1)^{Q_{aa}} \alpha^{|a|} \alpha'^{N-|a|}. \quad (18)$$

A glance at the QWGT expression (3) reveals a striking similarity to the latter matrix element.

3.2. Example

As a simple example meant to illustrate the correspondence between the H -matrix representation of a quantum circuit Ω and the actual operation of the circuit, consider

$$H = \begin{bmatrix} 1 & 1 & 1 \\ 0 & 0 & 1 \\ 0 & 1 & 1 \\ 1 & 0 & 0 \\ 1 & 1 & 1 \\ 1 & 1 & 0 \end{bmatrix}.$$

This matrix represents a circuit Ω comprising three gates (three columns) acting on three qubits (two rows per qubit),

$$U(\Omega) = g_3 g_2 g_1,$$

with the following unitaries:

$$g_1 = e^{-i(\theta/2)\sigma_Z^{(1)}\sigma_X^{(2)}\sigma_Y^{(3)}} = \frac{1}{\gamma} \left(\alpha I - i\alpha' \sigma_Z^{(1)} \sigma_X^{(2)} \sigma_Y^{(3)} \right),$$

$$g_2 = e^{-i(\theta/2)\sigma_Z^{(1)}\sigma_Z^{(2)}\sigma_Y^{(3)}} = \frac{1}{\gamma} \left(\alpha I - i\alpha' \sigma_Z^{(1)} \sigma_Z^{(2)} \sigma_Y^{(3)} \right),$$

$$g_3 = e^{-i(\theta/2)\sigma_Y^{(1)}\sigma_Z^{(2)}\sigma_Z^{(3)}} = \frac{1}{\gamma} \left(\alpha I - i\alpha' \sigma_Y^{(1)} \sigma_Z^{(2)} \sigma_Z^{(3)} \right).$$

The superscripts represent which qubit is being acted upon and we have omitted the tensor product symbols. The Pauli operators can be read off from the corresponding column entries in H . Thus the entry $(1\ 0)^t$ in the top position of the first column of H represents the $\sigma_Z^{(1)}$ Pauli matrix in g_1 , etc.

3.3. QWGTs and the Ising partition function

In [22] it was shown that the Ising partition function can be expressed in terms of a QWGT. Let A be the incidence matrix of the graph $G = (E, V)$, i.e.

$$A_{v,(i,j)} = \begin{cases} 1 & (v = i \text{ and } (i, j) \in E), \\ 0 & \text{else.} \end{cases} \quad (19)$$

Let us associate a binary vector

$$w = (w_{12}, w_{13}, \dots) \quad (20)$$

of length $|E|$ with the bond distribution $\{J_{ij} = \pm J\}$ by letting

$$w_{ij} = \frac{1 - J_{ij}/J}{2}, \quad (21)$$

so that w specifies whether edge (i, j) supports a ferromagnetic ($w_{ij} = 0$) or antiferromagnetic ($w_{ij} = 1$) bond. Thus we can give an equivalent definition of an instance of the Ising model (recall definition 1) as the data $\Delta \equiv (G, w)$.

Let

$$\lambda = \tanh(\beta J), \quad (22)$$

and define the $|E| \times |E|$ matrix

$$B = \text{dg}(w) = \begin{cases} w & \text{on the diagonal,} \\ 0 & \text{elsewhere.} \end{cases} \quad (23)$$

Writing the instance data as $\Delta \equiv (G, w)$, we then have (theorem 2 of [22])

$$\begin{aligned} Z_\Delta(\lambda) &= \frac{2^{|V|}}{(1 - \lambda^2)^{|E|/2}} \sum_{a \in \ker A} (-1)^{a^t B a} \lambda^{|a|} \\ &= \frac{2^{|V|}}{(1 - \lambda^2)^{|E|/2}} S(A, \text{dg}(w), \lambda, 1), \end{aligned} \quad (24)$$

$$= \frac{2^{|V|}}{(1 - \lambda^2)^{|E|/2}} \sum_{a \in \ker A} (-1)^{a \cdot w} \lambda^{|a|}, \quad (25)$$

where a in the sums ranges over all 0–1 vectors of length $|E|$ satisfying $Aa = 0$, where $a^t Ba = \sum_i a_i w_i a_i = a \cdot w$ (since $a_i = 0$ or 1) was used in the second equality, and where the QWGT definition (3) was used in the last equality.

This establishes the link between QWGTs and the Ising model partition function. Because of the similarity to the matrix element $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$, we expect to be able to relate the partition function to quantum circuits, via QWGTs. We take this up in the next section.

Definition 4. *An even subgraph of a graph G (or equivalently an Eulerian subgraph) is any subgraph of G whose vertices are of even degree. Equivalently, these are paths in G which begin and end at the same vertex, and which pass through each edge exactly once.*

Now, note that the sum in

$$S(A, \text{dg}(w), \lambda, 1) = \sum_{a \in \ker A} (-1)^{a \cdot w} \lambda^{|a|} \quad (26)$$

is over vectors that are in the kernel (nullspace) of A , which here means that only subgraphs having an even number of bonds emanating from all vertices are allowed, i.e. the sum is taken over all even subgraphs or, equivalently, all Eulerian subgraphs.

In this work we will sometimes refer to even or Eulerian subgraphs as cycles.

4. Connecting the Ising model partition function to the quantum circuit matrix element

Our goal in this section is to connect the partition function Z to $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$. To do so we will use a mapping found and described in detail in [28].

4.1. A circuit ansatz

Focusing on the representation of the quantum circuit given in equation (15) and of the partition function given in equation (25), we begin by asking ourselves if there exists some ansatz for the gate set g_k such that

$$U(\Omega) = \prod_{k=N}^1 g_k \stackrel{?}{\propto} \sum_a (-1)^{a \cdot w} \lambda^{|a|} \tilde{\sigma}_{Ha}, \quad (27)$$

where $\lambda = \tanh(\beta J)$. If such a form were possible, the two representations would be closely linked. Indeed, we can almost get this form. Let us take as an ansatz

$$g_k = \frac{1}{\sqrt{\lambda^2 + 1}} (\lambda I + \tilde{\sigma}_{b_k}), \quad (28)$$

i.e. the special case of equation (10) with $\lambda = \alpha'/\alpha$, or

$$\tanh(\beta J) = \tan(\theta/2). \quad (29)$$

Note that, because the inverse temperature β and the bond strength J are both positive, equation (29) restricts $(\theta/2) \bmod 2\pi$ to be in the range $(0, \pi/2) \cup (\pi, 3\pi/2)$, or $\theta \bmod 4\pi$ to be in the range

$$R \equiv (0, \pi) \cup (2\pi, 3\pi) \quad (30)$$

(the range for which $\tan(\theta/2) > 0$). Fortunately, this includes the case $\theta = 2 \arcsin(4/5) \approx 1.85 \in R$ (i.e., $\lambda = 4/3$), which, as noted above, allows a universal set of one- and two-qubit gates to be obtained.⁷ Thus, we have not restricted the generality of the class of quantum circuits so far. On the other hand, most $\theta \in R$ do not correspond to universal quantum circuits.

Next, we obtain from equation (15)

$$\begin{aligned} U(\Omega) &= \prod_{k=N}^1 \frac{1}{\sqrt{\lambda^2 + 1}} (\lambda I + \tilde{\sigma}_{b_k}) \\ &= \frac{1}{(\lambda^2 + 1)^{N/2}} \sum_a (-1)^{Q_{aa}} \lambda^{|a|} \tilde{\sigma}_{Ha}. \end{aligned} \quad (31)$$

After taking matrix elements $\langle \mathbf{0} | \cdot | \mathbf{0} \rangle$, we have, recalling equation (18),

$$\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle = \frac{1}{(\lambda^2 + 1)^{N/2}} \sum_{a \in \ker CH} (-1)^{Q_{aa}} \lambda^{|a|}. \quad (32)$$

Comparing equations (25) and (32), while using $\lambda = \alpha'/\alpha$, we see that a sufficient condition for them to be equal is to identify the incidence matrix A with CH via

$$\tilde{A} = CH, \quad (33)$$

where \tilde{A} is a matrix containing twice the number of rows of the incidence matrix A , but where each even row is a zero row, and each consecutive odd row is equal to a row of the original incidence matrix A , and to equate the exponents, i.e. find an edge distribution w that solves

$$a \cdot w \bmod 2 = Q_{aa} \quad \forall a \in \ker A, \quad (34)$$

where $Q = \text{lwtr}(H^t \tilde{A})$ (recall equation (17)). For then

$$\begin{aligned} \langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle &= \frac{1}{(\lambda^2 + 1)^{N/2}} \sum_{a \in \ker A} (-1)^{a \cdot w} \lambda^{|a|} \\ &= \frac{(1 - \lambda^2)^{(|E|/2)}}{(1 + \lambda^2)^{(|E|/2)2^{|V|}}} Z_{\Delta}(\lambda). \end{aligned} \quad (35)$$

Equation (35) is a key result of this paper, as it establishes the equivalence between quantum circuits and the Ising model, for bond distributions w that satisfy equation (34), and λ s that satisfy equation (29).

It has two consequences. First, if we are able to determine $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$, then we are able to determine the partition function $Z_{\Delta}(\lambda)$. Note that estimating $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ in general is BQP-complete [54] and thus something one could do with a universal quantum computer.

⁷ These observations were used in [28] to show that finding additive approximations of the signed generating function of Eulerian subgraphs over hypergraphs is BQP-complete.

Alternatively, if we had a way of classically computing $Z_{\Delta}(\lambda)$, then we would be able to classically simulate the quantum circuit Ω (if it were solving a decision problem) [55]. This latter alternative is the one we focus on in this paper.

4.2. Circuit-Ising model compatibility

The connections we established in the previous subsection between quantum circuits and the partition function imply certain restrictions. We flesh these out in the present subsection.

First, since we wish to work only with the physically relevant range of positive temperatures and positive J , we restrict the gate angles θ from now on to lie in R . Formally:

Definition 5. A gate angle θ (equation (6)) for a gate g_k (equation (10)) is said to be ‘ λ -compatible’ if $\theta \in R$, where the range R is defined in equation (30).

Next, we note that equation (33) gives rise to a compatibility relation between circuits and graphs:

Definition 6. A quantum circuit Ω , constructed with λ -compatible angles, is ‘ G -compatible’ with a graph G if the H -matrix representation of Ω satisfies equation (33), where A is the incidence matrix of G , and C is defined in equation (12).

When we take a G -compatible circuit and plug its H -matrix into equation (34), we are not guaranteed that there exists a solution w . Hence we need an appropriate restriction of the class of G -compatible circuits:

Definition 7. A quantum circuit Ω is ‘ Gw -compatible’ if it is G -compatible and if the solution set of equation (34) is non-empty.

We need a similar notion for the bond distributions:

Definition 8. A bond distribution w is ‘ $G\Omega$ -compatible’ with a graph G and circuit Ω if it satisfies equation (34).

Note that in this last definition the circuit Ω must be Gw -compatible, for otherwise we are not guaranteed that the solution set of equation (34) is non-empty. Note further that, as these definitions imply, equations (33)–(35) describe a connection between quantum circuits and instances of the Ising model over given graphs. Namely, any H that solves equation (33) is a matrix representation of a circuit Ω which belongs to a class defined by the incidence matrix A of a given graph G . In addition, we can populate the vertices of G with weights from the bond distribution w provided w is compatible. Thus:

Definition 9. Let Γ be any set of graphs for which a solution to equation (34) exists. Then $\Omega_{\Gamma w}$ is the set of circuits which are Gw -compatible $\forall G \in \Gamma$.

Definition 10. $I(\Omega_{\Gamma w})$ is the class of Ising model instances $\{\Delta(G, w)\}_w$ whose graph is $G \in \Gamma$ and whose bond distributions $\{w\}$ are $G\Omega$ -compatible $\forall G \in \Gamma$.

Equation (34) is a system of linear equations over $\text{GF}(2)$. The number of equations is equal to the number of even subgraphs of the given graph or the total number of elements in the set $\ker(CH)$, and the number of unknowns is equal to the number of edges. However, in spite of the fact that the number of elements in $\ker(CH)$ scales exponentially in the number of vertices, it

turns out that finding a w which solves equation (34) can be done efficiently (see equation (38) below). Let us further stress that equation (34) is only a sufficient condition for the equality of equations (25) and (32), and does not capture the whole set of possible graph instances that our scheme can handle. We define our instances via this condition because it simplifies the analysis and it allows us to extract information about an interesting set of quantum circuits which may be classically simulated. We discuss more general sufficient conditions in section 7.1, but leave the development of a complete understanding of the actual graph instances that our mapping can handle, and in particular finding necessary conditions for the equality of equations (25) and (32), as a problem for future study.

5. Circuits corresponding to certain planar graphs have an efficient classical simulation

Let us recap the general idea we have developed so far. At the basis of our construction are an inverse temperature β , a bond strength J and a given graph G . We use this graph to first identify a compatible class of quantum circuits Ω_G (definition 9). This class is restricted to a subclass Ω_{Gw} of circuits for which there exist solutions to equation (34). Such solutions are used to assign weights to the graph's edges (a bond distribution), which yields a class of Ising model instances compatible with G and Ω_G (definition 10). In other words, we go from the unweighted graph to a class of compatible circuits, and from there back to the graph, which is now populated by a class of compatible Ising models. Each circuit is also parametrized by an angle θ , and when we vary θ in the range R [equation (30)] we also vary over β and J , via $\tan(\theta/2) = \tanh(\beta J)$. However, not all values of θ correspond to universal circuits. Conversely, not every circuit needs to correspond to a physical (positive) temperature.

In more detail, we identify the class of quantum circuits Ω_G compatible with G (whose incidence matrix is A) by solving equation (33) for the matrices H representing each (or some) $\Omega \in \Omega_G$, and then find the subset Ω_{Gw} for which the solution set to equation (34) is non-empty. We then look for a bond distribution w that satisfies equation (34) for a given H . Every such w defines an Ising model instance $\Delta(G, w)$ that is compatible with G and the corresponding $\Omega \in \Omega_G$. We are guaranteed that, provided such a bond distribution w exists, the partition function for the corresponding Ising model is proportional to the matrix element $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ (equation (35)). In other words, any bond distribution w that satisfies equation (34) induces a direct connection between quantum computation and the Ising model on a graph with that same bond distribution.

It is important to emphasize that equation (34) will not always have a solution w . Whether or not this is the case is entirely determined by the given graph G , since G , via its incidence matrix A , determines the class of G -compatible circuits Ω_G (i.e. the matrices H that solve equation (33)), and together they determine a and Q_{aa} that go into equation (34), which w needs to solve. Thus, it makes sense to define a class of graphs for which there exists a solution w to equation (34).

Definition 11. Θ is the set of graphs for which a solution to equation (34) exists.

In order to characterize Θ , we require some basic ideas from graph theory, such as obstruction sets and downward closure. These are reviewed in appendix A. We shall prove:

Lemma 1. *The obstruction set for Θ is finite.*

This will come as a consequence of Θ 's downward closure and the Robertson–Seymour theorem—theorem 3. This is proved in appendix B. We shall also prove there that there are no solutions to equation (34) for the graphs K_4 (the complete graph on four vertices) and $\bar{K}_{3,3}$, where $\bar{K}_{3,3}$ is $K_{3,3}$ with one edge missing (which edge does not matter, since upon deletion of another edge one can just relabel the edges and nodes and obtain exactly the same incidence structure). Formally:

Lemma 2. *The obstruction set for Θ includes $\bar{K}_{3,3}$ and K_4 .*

The proof is described in appendix B. See figure 2 in appendix A for a pictorial representation of the graphs mentioned in lemma 2. As a consequence we will find that *all graphs in Θ are planar*.

We shall clarify this conclusion and the previous lemma in the next subsection, but given their validity, a $G\Omega$ -compatible bond distribution w necessarily corresponds to a *planar* graph G . From this it follows that its partition function can be efficiently computed classically, and hence the corresponding class of quantum circuits, i.e. $\Omega_{\Theta w}$, also has an efficient classical simulation. Let us be precise about what we mean by ‘classically efficiently simulatable’ (CES).

Definition 12. *A uniform family $\mathcal{G}_n = \{\Omega_i\}$ of n -qubit quantum circuits is CES if the matrix element $|\langle \mathbf{0} | U(\Omega_i) | \mathbf{0} \rangle|$ of each circuit in \mathcal{G}_n can be obtained to k digits of precision in time $\text{poly}(n, k)$ by classical means [55].*

This definition is a modified version of the one given in [36], which also includes a discussion on how it can be weakened.

When we collect the observations above, we arrive at an efficient classical test for whether a given quantum circuit is CES. This is summarized in theorem 1, which is our main result and the subject of the remainder of the paper:

Theorem 1 (Circuits corresponding to certain planar graphs have an efficient classical simulation). *The class of quantum circuits $\Omega_{\Theta w}$ is CES. Deciding whether a given graph G is in Θ can be efficiently decided.*

The theorem comprises two parts. In the first, we characterize an entire class of CES quantum circuits. The proof we offer below is not constructive, i.e., we prove that there *exists* an efficient classical simulation of the class of quantum circuits $\Omega_{\Theta w}$, and also provide a test of non-membership in $\Omega_{\Theta w}$ for a given quantum circuit. In the second part we give an explicit construction which decides whether a given graph belongs to the set of graphs resulting in CES circuits. To illustrate this part, we discuss a class of graphs (which is a subset of $\Omega_{\Theta w}$) in section 6, for which we can explicitly find the $G\Omega$ -compatible bond distribution. This class is highly restricted in that the number of even subgraphs only grow polynomially in the number of vertices, whereas in general, including restrictions to planar graphs, the number of even subgraphs grow exponentially. Nonetheless, the class of quantum circuits that one obtains under this restriction is interesting in light of some new results about the classical simulatability of quantum circuits [36, 42].

For the benefit of the reader, we summarize the scheme of the first claim of the proof informally. This will also serve to summarize again the mapping between quantum circuits and graphs.

1. **Given:** any subset Γ of Θ . Every $G \in \Gamma$ has a $G\Omega$ -compatible bond distribution for some quantum circuit Ω , by assumption.
2. Take the incidence matrices CH of the graphs in Γ and transform them into the *H-matrix representations* of the corresponding quantum circuits. The following constraint must be respected: every column must have one *Y-operation* and can have at most one *X-operation*. (This constraint comes from the fact that CH should be an incidence matrix for a graph, where C is the block-diagonal matrix defined in equation (12). Without it one has a correspondence between quantum circuits and hypergraphs [28]. Indeed, if the incidence matrix has more than two ones per column, then one has a hypergraph.)
3. Thus Γ corresponds to a set of quantum circuits $\Omega_{\Gamma w}$, i.e., every quantum circuit $\Omega \in \Omega_{\Gamma w}$ is *Gw-compatible* for some $G \in \Gamma$.
4. Show that our mapping from circuits to graphs defines a ‘downward closed set’ of graphs, which means that we may apply the Robertson–Seymour theorem [56]. This theorem guarantees that there is a finite set of graphs (obstruction set) for which we can test whether or not G has any members of this set as a graph minor [57] (at most cubic complexity in the number of quantum gates in Ω).
5. Define Θ , via this obstruction set, i.e., a graph is a member of Θ if it does not have K_4 and $\bar{K}_{3,3} = K_{3,3}$ with one edge deleted as minors (there may be other forbidden minors). One has a set of circuits that correspond to the graphs which have a satisfying bond distribution w for equation (34). We call the corresponding class of quantum circuits $\Omega_{\Theta w}$. (This specific obstruction set has been tested with mathematical software. See appendix C.)
6. Owing to the fact that these graphs are planar, the partition function Z of any graph in Θ can be computed efficiently by a classical computer [50].
7. Using equation (35), show that knowledge of Z can be used to determine the outcome of a quantum circuit $\Omega \in \Omega_{\Gamma w}$ for a decision problem.
8. **Conclude:** Families of quantum circuits in $\Omega_{\Theta w}$ which solve a decision problem can be classically simulated.

Being that Γ is a subset of Θ , any subset $\Omega_{\Gamma w}$ of $\Omega_{\Theta w}$ is CES.

Conversely, we have a test for non-membership in the set $\Omega_{\Theta w}$:

1. **Input** a quantum circuit Ω .
2. Transform Ω into a matrix whose columns represent Pauli operations (that are to be exponentiated) and every pair of rows are the qubits being acted upon, as described in section 3.1. This matrix is called H (equation (14)) and is in one-to-one correspondence with Ω . As above, the following constraint must be respected: every column must have one *Y-operation* and can have at most one *X-operation*.
3. After the above transformation, construct a corresponding incidence matrix CH of a graph G .
4. Check the graph G for the minors $\bar{K}_{3,3}$ and K_4 .
5. **Conclude:** If either of these are minors of G then reject Ω .

5.1. Ordering lemma

The following lemma allows us to introduce an ordering on the elements in Θ .

Lemma 3. *If a graph G is a member of Θ , then so is $G \setminus e_j$ or G/e_j , i.e., the deletion or contraction of an arbitrary edge e_j from a graph in Θ is also in Θ .*

The proof of this lemma is technical and is given in appendix B. Lemma 3 implies that Θ is a *downwardly closed set with respect to the minor ordering*. Hence we can apply the Robertson–Seymour theorem [56], theorem 3, which states that *any graph may be tested for membership in a given downwardly closed set of graphs by just searching the graph for a finite set of minors*. The complexity of doing this, given knowledge of the minors one is looking for, can be shown to be cubic in the number of edges. We implemented this to test equation (34) for non-planar solutions, as we describe next.

5.2. Equation (34) implies planarity

Using mathematical software, we demonstrated that the mapping between graphs and circuits described above, with the sufficient condition given by equation (34), cannot be satisfied for K_5 and $K_{3,3}$. That is, K_5 and $K_{3,3}$ are forbidden minors for Θ . The algorithm we implemented to check this is described in appendix C. However, a finite graph is planar if and only if it does not have K_5 or $K_{3,3}$ as minors (Wagner’s theorem; see appendix A). As stated earlier, we thus have:

Lemma 4. *All graphs in Θ are planar.*

We note that we have been able to find examples of planar graphs for which there do exist solutions w to equation (34), e.g. $K_{2,3}$. As we explain below, this means that Θ includes planar graphs which are not outerplanar.

5.3. Knowledge of the matrix element determines output to a decision problem

The standard way in which a quantum circuit U solves a decision problem is to measure, say, the first qubit and decide the problem according to this measurement outcome. In [55] it was shown that, for every such decision problem, there exists another quantum circuit U' , such that the evaluation of $\langle 0|U'|0\rangle$ is equivalent to the decision problem solved by applying U and measuring the first qubit. In this sense we have:

Lemma 5. *Knowledge of $\langle 0|U(\Omega)|0\rangle$ suffices to determine the output of a quantum circuit which is being used to solve a decision problem.*

For a proof see, e.g., [55].

5.4. Proof of theorem 1

Collecting everything we now prove our main theorem. We first need one more technical lemma:

Lemma 6. *A quadratic form x^tAx over $GF(2)$ is linear in x (equal to $x^t \text{diag}(A)$) iff A is symmetric.*

Here $\text{diag}(A)$ denotes a vector comprising the diagonal of A . The proof of this lemma is presented in appendix B.

Proof of theorem 1. We start from the second claim of the theorem, namely we prove that we can efficiently decide whether a given graph belongs to the set Θ , and that we can find some w if it does belong. Let G be a given graph and let K be the matrix whose columns are a basis of $\text{Ker}(A)$, where A is the incidence matrix of G . This means that any $a \in \text{Ker}(A)$ may be written as $a = Kx$ where x is an arbitrary $m = \dim(\text{Ker}(A))$ -dimensional binary vector. Using this we may rewrite equation (34) over $\text{GF}(2)$ as

$$x^t K^t Q K x = (Kx)^t w. \quad (36)$$

Since the right-hand side is linear in x for all x , the left-hand side must also be linear in x . It follows by lemma 6 that $K^t Q K$ is symmetric and, moreover, that the quadratic form $x^t K^t Q K x$ can be written as $x^t \text{diag}(K^t Q K)$. Thus, solving equation (34) for w is equivalent to solving the linear system $x^t K^t w + x^t \text{diag}(K^t Q K) = 0$, or

$$x^t (K^t w + \text{diag}(K^t Q K)) = 0. \quad (37)$$

Because this equation must be true for all x , it follows that $K^t w + \text{diag}(K^t Q K) = 0$ and hence that w is the solution to

$$K^t w = \text{diag}(K^t Q K). \quad (38)$$

Since K is efficiently constructable, w can also be found efficiently using standard methods for solving linear equations over $\text{GF}(2)$.

Now for the first claim of the theorem, which states that the circuits corresponding to the graphs in Θ are CES. Θ is a downwardly closed set of graphs, which generates a set of compatible quantum circuits $\Omega_{\Theta w}$ via the mapping described above. In turn we have the corresponding Ising model instances $I(\Omega_{\Theta w})$ and, by assumption, the $G\Omega$ -compatible bond distributions w for the circuits $\Omega \in \Omega_{\Theta w}$ and graphs $G \in \Theta$. Lemma 4 states that these instances are planar. It follows that they are CES, i.e., we may compute the Ising partition function for any of these instances efficiently with a classical computer. This is due to a result by Kasteleyn, who gave a classical algorithm for the exact evaluation of the Ising partition function of any planar graph in the absence of an external magnetic field [58]. According to our definition of CES quantum circuits (definition 12), all we need is to be able to obtain the evaluation in a polynomial time in the number of qubits (which translates to the number of vertices), and the desired number of bits of precision of $Z_{\Delta}(\lambda)$, which is achieved by the algorithm given in [58]. Now, because $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle \propto Z_{\Delta}(\lambda)$ (equation (35)), and for any graph in Θ we have an efficient way of classically determining $Z_{\Delta}(\lambda)$, we are thus able to determine the matrix element $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ for any quantum circuit in $\Omega_{\Theta w}$ efficiently. It follows from lemma 5 that any quantum circuit in $\Omega_{\Theta w}$ which solves a decision problem is CES. \square

We note that this technique can be used to prove that quantum circuits that correspond to non-planar classes of graphs for which the Ising partition function has efficient classical evaluation schemes, e.g., graphs of bounded tree width, are CES. We suspect that some of the results obtained in [39] may be reproduced in this way.

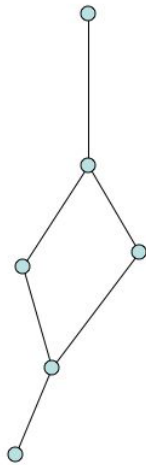


Figure 1. A graph with only one cycle.

6. Further characterization of the class of classically efficiently simulatable (CES) quantum circuits $\Omega_{\Theta w}$

Our motivation in this subsection is to present a result on CES quantum circuits which allows a comparison to the recent results presented in [36, 42]. In both papers, results dependent on quantum gates being restricted to nearest-neighbor qubit operations in one dimension are presented. Via our construction we derive a similar result, but show that the restriction to nearest-neighbor operations and one dimension can be lifted. We begin with a simple example.

6.1. Graphs in Θ are compatible with CES circuits which include non-nearest-neighbor operations

Recent work in [42] demonstrates that any circuit that is built out of X -rotations and nearest-neighbor $Z \otimes Z$ rotations can be efficiently simulated. Now assume that one is restricted to a class of planar graphs, Θ_p , for which the number of even subgraphs scales polynomially with the number of vertices. This restriction is not necessary and is introduced merely for simplicity. Let us call the corresponding set of quantum circuits (under the mapping presented above) Ω_p . Upon inspection of the incidence matrix of a typical graph in Θ_p , one sees that, even though the majority of incident vertices are nearest neighbor, there are several that are not, no matter how one labels the vertices. For example, consider the graph depicted in figure 1.

The incidence matrix is given by

$$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 \\ 1 & 1 & 0 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 \end{bmatrix}$$

and one possible circuit representation H is given by

$$\begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 \\ 1 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 \\ 1 & 1 & 0 & 0 & 0 & 1 \\ 0 & 1 & 1 & 0 & 0 & 0 \\ 0 & 1 & 1 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 1 & 1 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & 1 & 0 & 0 \\ 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 1 & 1 \end{bmatrix}.$$

Note that the fifth and sixth columns correspond to gates of the form

$$e^{-i\theta(\sigma_x^{(4)} \otimes \sigma_y^{(6)})} \quad \text{and} \quad e^{-i\theta(\sigma_y^{(2)} \otimes \sigma_x^{(6)})},$$

respectively. The superscripts indicate which qubit is being operated on, and thus one can clearly see that *non-nearest-neighbor interactions are possible*. This example demonstrates that our construction may extend the results in [42], for example, by linking together graphs like the ‘necklace’ shown. Note, however, that the nearest-neighbor restriction can only be relieved slightly as most of the interactions will in fact remain nearest neighbor. Taking this example as motivation, what follows is a more general construction which will be used to pursue a better understanding of CES circuits.

6.2. A class of CES circuits with non-nearest neighbor gates

We now define a simple subclass of planar graphs which have a polynomial (in the number of vertices) number of even subgraphs. Because of planarity, this class of graphs corresponds to CES quantum circuits. However, note that this class of graphs is by no means an exhaustive characterization of all planar graphs which have a polynomial number of even subgraphs.

Definition 13. A basis of the null space of the incidence matrix CH is referred to as a cycle basis.

Definition 14. Let Θ_{pc} be those planar graphs with V vertices and $E = V + O(\log V^k)$ edges, where $k \in \mathbb{R}^+$.

Proposition 1. Θ_{pc} has a polynomial, in V , number of even subgraphs.

Proof. A cycle basis consists of a set of connected even subgraphs of a given graph in Θ_{pc} . The dimension of the null space (or the number of elements of the cycle basis) is in this case equal to the number of edges minus the rank of CH . Recall also that the rank of the incidence matrix equals the number of edges minus the number of components (which is one in our case). Thus, asymptotically, one has

$$\text{nullity} = V + O(k \log V) - \text{rank}(CH) = O(\log V^k). \quad (39)$$

Now, note that the null space allows all possible sums of the basis and therefore we are left with $O(V^k)$ elements as claimed. \square

One could imagine graphs in Θ_{pc} as being sparse graphs consisting of cycles (even subgraphs) strung together along trees without too many branching points. This is due to the relationship between E and V given above. One can see that a branch without a cycle always adds an additional vertex (one edge has two vertices) and the only way that the relationship between vertices and edges can be satisfied is if the number of branches is kept smaller than the number of cycles. That is, there will need to be *more cycles than edges that do not terminate at a cycle*. Further, the incidence matrix of these structures, like the example above in figure 1, will have columns that consist of nearest-neighbor consecutive ‘1s’ for the majority of positions. This rule is broken when a tree branches and when one runs into a cycle. By theorem 1, this can only happen $O(\log V^k)$ times. As E is the number of gates and V is the number of qubits in the corresponding quantum circuit, we have just proven:

Corollary 1. *A quantum circuit consisting of gates of the form $e^{-i\theta(X^{(i)} \otimes Y^{(j)})}$, which act on nearest-neighbor qubits except for $O(k \log(\# \text{ of qubits}))$ gates, which can act on qubits i and j such that $|i - j| \geq 2$, is CES.*

Note: Z operations may be included in the exponent of this operator at any position not occupied by the X and Y operations.

The important point in the last corollary is that we allow non-nearest-neighbor gates, thus extending the results of [42], and also [59], where only nearest-neighbor CES quantum circuits were considered.

6.3. Θ includes some but not all outerplanar graphs

So far we have stressed the special role of $K_{3,3}$ and K_5 , which led us to the conclusion that all graphs in Θ are planar. However, it turns out that we can be more specific, since we have also been able to show that K_4 and $\bar{K}_{3,3}$ are forbidden minors for Θ (lemma 2; see appendix C for a description of the proof, using mathematical software). In other words, there does not exist a solution to equation (34) for the graphs K_4 and $\bar{K}_{3,3}$. These graphs play a role in characterizing the set of *outerplanar graphs* [57], which we define next:

Definition 15. *For any planar graph, there are regions bounded by the cycles of the graph and an unbounded region outside of all the cycles. An outerplanar graph is a planar graph for which every vertex is within the unbounded region when it is embedded in the plane such that no edges intersect.*

For example, the graph in figure 1 is outerplanar. More informally, a graph is outerplanar if it can be embedded in the plane such that all vertices lie on the outer (exterior) face. A graph G is outerplanar iff $K_1 + G$ (a new vertex is connected to all vertices of G) is planar [60]. The characterization of relevance to us is the following analog of Kuratowski’s theorem for planar graphs (described in appendix A):

Theorem 2 (Chartrand and Harrary [61]). *A graph is outerplanar if and only if it has no subgraph homeomorphic to K_4 or $K_{2,3}$.*

In other words, $K_{2,3}$ is a forbidden minor for outerplanar graphs, where $K_{2,3}$ is like $K_{3,3}$ except that one side of the bipartite graph has two vertices instead of three.

Proposition 2. *If a graph is outerplanar then it does not have $\bar{K}_{3,3}$ as a minor.*

Proof. Assume that $\bar{K}_{3,3}$ is a minor of some $G \in \text{Outerplanar}$. Then G has $K_{2,3}$ as a minor, since $K_{2,3}$ is a minor of $\bar{K}_{3,3}$. (This is easy to see: just contract one edge and delete another.) But by theorem 2, outerplanar graphs cannot have $K_{2,3}$ as a minor, which is a contradiction. Thus G cannot be outerplanar. \square

Note that the converse is not necessarily true, i.e., not all graphs which do not have $\bar{K}_{3,3}$ as a minor are outerplanar.

Proposition 3. *Outerplanar Graphs $\cap \Theta \neq \emptyset$ and Outerplanar Graphs $\neq \Theta$.*

Proof. By lemma 2, if $G \in \Theta$ then G cannot have K_4 or $\bar{K}_{3,3}$ as minors. By theorem 2 and proposition 2, if G' is an outerplanar graph then it cannot have K_4 or $\bar{K}_{3,3}$ as minors either. This suggests that Θ may have graphs in common with the set of outerplanar graphs. We have verified, using mathematical software, that the intersection is indeed non-empty. For example, we have found that certain trees with cycles, which are outerplanar by construction, are in Θ . Moreover, we have verified using mathematical software that $K_{2,3} \in \Theta$, i.e., it is not a forbidden minor for the existence of a solution w . But $K_{2,3}$ is not outerplanar, hence there are graphs in Θ which are not outerplanar (in particular, all subdivisions of $K_{2,3}$). \square

This is interesting since some problems are NP-complete for subclasses of planar graphs but solvable in polynomial time for outerplanar graphs. Some examples are the chromatic number, Hamiltonian path and Hamiltonian circuit. Another example is the page number which is one for outerplanar graphs. This means that we can embed the vertices on a line which divides the plane into two subplanes, and draw all edges in one of the subplanes without crossing. In a sense, outerplanar graphs are ‘easy’ computationally. The fact that Θ includes non-outerplanar graphs thus suggests that it may include interesting computational problems.

7. Discussion and future directions

In this section we briefly discuss two possible future directions for research.

7.1. General condition for the bond distribution

So far we have assumed the sufficient condition (34) in order to obtain the desired equality between the partition function and the circuit matrix element $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ (equations (25) and (32)). We have also shown that a satisfying bond distribution w can always be efficiently tested for and computed, under equation (34). Let us now relax the constraint of equation (34) by considering a more general way in which the desired proportionality,

$$\sum_{a \in \ker A} (-1)^{a \cdot w} \lambda^{|a|} \propto \sum_{a \in \ker CH} (-1)^{a \cdot \text{twr}(H^T CH) a} \lambda^{|a|}, \quad (40)$$

can be obtained. Indeed, equation (34) is clearly not a necessary condition. The following construction demonstrates that it is likely that the number of cases which *do not have a solution* w for the bond distribution is much smaller than the case we analyzed given by equation (34).

Note that in equation (40) the powers of the λ s are the weights of the null vectors a , that is the number of ones in a . Thus it is possible for an equality to occur for a given term in the sum

in the two sides of equation (40) for different as , as long as the weights of the as are equal. This gives us the constraint for the following. One can organize all the as in bins in terms of weights from 1 to $|E| = N$. Let us now take bin r , i.e., the set of vectors of weight r . Let a_{r_1}, \dots, a_{r_n} be all the null vectors of CH of weight r . Then, if for all r

$$\{a_{r_{j_1}}^\dagger \text{lwtr}(H^\dagger CH) a_{r_{j_1}} = a_{r_{j_2}} \cdot w\} \wedge, \{a_{r_{j_2}}^\dagger \text{lwtr}(H^\dagger CH) a_{r_{j_2}} = a_{r_{j_3}} \cdot w\} \wedge \dots \wedge, \\ \{a_{r_{j_n}}^\dagger \text{lwtr}(H^\dagger CH) a_{r_{j_n}} = a_{r_{j_1}} \cdot w\}, \quad (41)$$

where $\{j_1, \dots, j_n\}$ is any permutation of the numbers $\{1, \dots, n\}$, then equation (40) would be satisfied. Clearly, equation (34) is a special case of this more general condition. This demonstrates that it is likely that a satisfying bond distribution w can be found for a given graph, even if the sufficient condition (34) cannot be satisfied. Loosely, this is due to the fact that there are many conjunctive statements of the form (41) that can be satisfying. In fact, it seems likely that also non-planar graphs (i.e. those containing K_5 or $K_{3,3}$ as minors) may have a satisfying w according to equation (41). We leave this as a problem for future investigation. An important point concerning the more general condition (41) is that we do not know if it can be efficiently tested for a satisfying bond distribution w , let alone solved for such a w .

7.2. Computing the Ising partition function

As mentioned in section 4.1, equation (35) has two consequences, and our focus in this paper has been on the ability to find CES circuits using known results about the hardness of computing partition functions. Let us now briefly consider the other consequence, namely the fact that, if we are able to determine $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$, then we are able to determine the partition function $Z_\Delta(\lambda)$.

A fully polynomial randomized approximation scheme (fpras) for the fully ferromagnetic Ising partition function was presented in [62]. It is well known that having an fpras for the non-ferromagnetic Ising model implies that $\text{NP} = \text{RP}$ (randomized polynomial time), which would be quite unexpected [50]. It should therefore be of no surprise that no fpras for this problem has been found, even with quantum resources. However, *additive* approximation schemes seem likely and, in fact, one was given in [23] for the related Potts model partition function, even though the instances that they were able to account for are not known to be BQP-complete and the hardness is, in fact, unknown.

Equation (35) precisely relates a matrix element of a quantum circuit to the value of the partition function of the Ising model for a corresponding graph instance. This means that, if we could approximate the matrix element, we would have an approximation for the Ising partition function. Due to the Hadamard test, it is well known that a polynomial estimation of this matrix element is BQP-complete. (See [54] for a description of the Hadamard test.) Specifically, by making $1/\epsilon^2$ measurements, one can have either $\text{Re}\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ or $\text{Im}\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ to precision ϵ , but one must keep in mind that this approximation is an additive one. This means that, with some probability of success bounded below (say by 0.75), the approximation returns m such that

$$\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle - \delta \cdot p < m < \langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle + \delta \cdot p, \quad (42)$$

where p is a polynomially small parameter and δ is the approximation scale of the problem. Note that if $\delta = O(\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle)$ then the approximation will be an fpras [23]. Equations (35) and (42) taken together quantify how a measurement of $\langle \mathbf{0} | U(\Omega) | \mathbf{0} \rangle$ yields an approximation of the partition function of the Ising model instance Δ corresponding to the circuit Ω .

8. Conclusions

We have provided a construction that allows one to determine if a given quantum circuit corresponds to a class of quantum circuits which are CES. This was done by looking at the corresponding graph instances of the classical Ising model using a mapping previously introduced in [28]. This was then used to conclude that any class of quantum circuits which solve decision problems and are restricted to certain planar graph instances are CES. Our main result is stated in theorem 1, which characterizes the class of CES circuits via the set of planar graphs Θ . We have given a partial characterization of Θ by stating that its obstruction set includes $\bar{K}_{3,3}$ and K_4 (and hence, by downward closure, also $K_{3,3}$ and K_5). An interesting open problem is to give a complete characterization of the obstruction set; we know from the Robertson–Seymour theorem that this set is finite, since we have proved that Θ is downwardly closed.

Our mapping can also be used to construct a quantum algorithm for the additive approximation of the partition function. However, there are two issues. The instances we are able to handle are constrained by our use of equation (34), which does not capture all the ways a certain bond distribution may satisfy equation (40), but which simplifies our analysis greatly. The other issue is the fact that our mapping may fail to provide information about the bond distribution of a given graph.

An open problem is to obtain a better understanding of what the complexity of finding the bond distribution for a particular graph instance is. This understanding will have consequences in our knowledge of where BQP is in the complexity hierarchy, as we will be able to relate the simulatability of universal quantum circuit families with the complexity of finding bond distributions. On the other hand, it is possible that the complexity of finding bond distributions is somehow incorporated into the power of the quantum circuit that corresponds to the graph instance of the Ising model, in the sense that the circuit corresponding to a planar graph (under our mapping) may not be CES, because the effort of obtaining the bond distribution via equation (41) blocks such a simulation.

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Appendix A. Essential elements from graph theory

Here we review some essential definitions and theorems from graph theory needed for the results presented in this work. A good reference for these concepts is the Wikipedia article on planar graphs, or [57].

Definition 16. *A subgraph H of a given graph G is called a minor (or child) of G if it is isomorphic to a graph that can be obtained from G via a sequence of edge deletions, edge contractions or deletion of isolated vertices. Edge contraction is the process of removing an edge and combining its two endpoints into a single vertex. Edge deletion removes an edge without removing its vertices.*

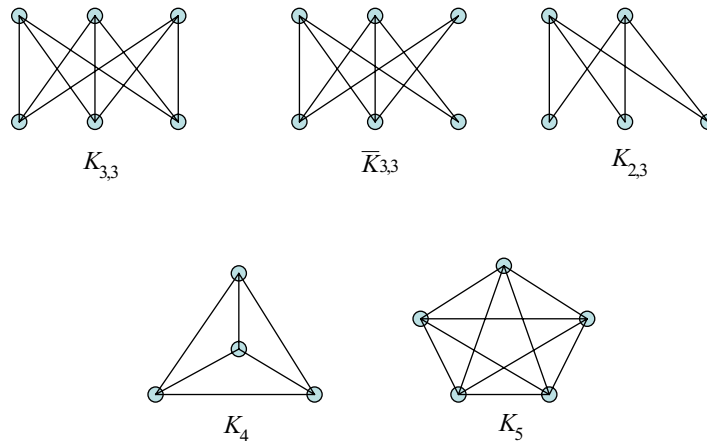


Figure 2. The various graphs playing a role in defining the obstruction set for Θ and outerplanar graphs.

Definition 17. *The set of graphs S is downwardly closed with respect to minor ordering if, whenever G is a member of S , then so is any minor of G .*

A trivial consequence of the definition of downwardly closed sets is that every such set has an obstruction set:

Corollary 2. *An obstruction set for G is a set of minors of G , also called forbidden minors, with the property that they prevent downward closure.*

In other words, if one constructs a set of minors of G and encounters a minor that violates the property for which downward closure is being tested, such a minor is called forbidden and belongs to the obstruction set. Now comes a seminal theorem due to Robertson and Seymour [56]:

Theorem 3. (Robertson–Seymour) *Every downwardly closed set of graphs (possibly infinite) has a finite obstruction set, i.e., a finite set of forbidden minors.*

For our purposes, an important example are the planar graphs:

Definition 18. *A planar graph is a graph which can be embedded in the plane, i.e., it can be drawn on the plane in such a way that its edges may intersect only at their endpoints, i.e., edges never cross. A non-planar graph is a graph which cannot be drawn in the plane without edge intersections.*

There are two particularly important non-planar graphs, denoted K_5 (the complete graph on five vertices; complete means that each pair of vertices are connected by an edge) and $K_{3,3}$ (the complete bipartite graph on six vertices, three of which connect to each of the other three). They are depicted in figure 2.

Planarity is characterized by Wagner’s theorem [57]:

Theorem 4 (Wagner). *A finite graph is planar if and only if it does not have K_5 or $K_{3,3}$ as a minor.*

In other words, if one deletes or contracts the edges of a graph and finds one of these minors, then the graph is not planar. Hence K_5 and $K_{3,3}$ are forbidden minors (form an obstruction set) for planarity.

For completeness, we note that an alternative characterization can be given in terms of the concept of a subdivision of a graph:

Definition 19. A subdivision of a graph results from inserting vertices into edges.

Thus, while deletion and contraction of edges shrinks a graph down, subdivision builds it up.

Definition 20. Two graphs G and G' are homeomorphic if there is an isomorphism from some subdivision of G to some subdivision of G' .

Theorem 5 (Kuratowski). A finite graph is planar if and only if it has no subgraph homeomorphic to K_5 or $K_{3,3}$.

In other words, a finite graph is planar if and only if it does not contain a subgraph that is isomorphic to a subdivision of K_5 or $K_{3,3}$.

In this work we give a criterion for graph membership (in a certain set Θ , which is defined in definition 11) based on the existence of a solution for the system of linear equations over $GF(2)$ defined by equation (34). We want to be guaranteed that this membership has an ordering in the sense that, if G is a member, then so is every minor of G . In other words, we are testing for downward closure. The Robertson–Seymour theorem guarantees that membership in our set Θ is not obstructed by an *infinite* set of graphs. However, the situation is in fact far better: membership of a graph to a fixed downward closed set can be checked by running a polynomial time algorithm for all elements of the obstruction set (if it is known), since searching for a minor on a given graph only requires cubic time [57]. In fact, checking whether a graph is planar can be done in linear time.

For completeness, we include the definition of a *hypergraph*, as our correspondence between quantum circuits and graphs is actually a mapping between circuits and hypergraphs [28].

Definition 21. A hypergraph is a generalization of a graph where edges are replaced by hyperedges. Let $V = \{v_1, v_2, \dots, v_k\}$ be the set of vertices and let $E = \{e_1, e_2, \dots, e_n\}$ be the set of hyperedges. Each $e_i = \{v_{i1}, v_{i2}, \dots, v_{im}\}$ is a collection of vertices where each $v_{ij} \in V$.

Thus the main difference from ordinary graphs is that edges consist of arbitrary collections of vertices rather than two, and thus graphs are special cases of hypergraphs. As shown in [28], the existence of hyperedges is what gives us access to the universal gate set presented in [49], via the two assumptions enumerated at the end of section 4. However, the circuits that correspond to graphs is what is interesting here, as our results depend on information about the Ising partition function defined on ordinary graphs.

Appendix B. Proof of lemmas

Here we present the proof of lemmas 1, 3 and 6, which we repeat for convenience:

Lemma 3. If a graph G is a member of Θ , then so is $G \setminus e_j$ or G/e_j , i.e., the deletion or contraction of an arbitrary edge e_j from a graph in Θ is also in Θ .

Proof. Assume that $G \in \Theta$. Recall that a graph G is an element of Θ if there exists some solution w to the set of linear equations over $\text{GF}(2)$,

$$A^{(G)}w = \alpha^{(G)}, \tag{B.1}$$

where $A^{(G)}$ is the matrix whose rows are elements, a_i , of the nullspace of the incidence matrix of the graph G (given as the Ising instance), and $\alpha^{(G)}$ is the vector whose entries are the $a_i \text{!wtr}(H^tCH)a_i$. These null elements, a_i , correspond to the even subgraphs of G and will be referred to as cycles (recall definition 4). From elementary linear algebra, we know that a solution exists if $\alpha^{(G)}$ may be written as a linear combination of columns of $A^{(G)}$ or, in other words, if

$$\text{Rank}[A^{(G)}|\alpha^{(G)}] = \text{Rank}[A^{(G)}]. \tag{B.2}$$

We must demonstrate that, after we either delete or contract an edge and arrive at the subgraph G' , we have

$$\text{Rank}[A^{(G')}|\alpha^{(G')}] = \text{Rank}[A^{(G')}] \tag{B.3}$$

We shall demonstrate this with an edge deletion as the case of a contraction is similar. We begin with a given graph G which is a member of Γ_w . We have

$$(G'^t CG')^{(G)} = \begin{bmatrix} G'_{11} & G'_{21} & \cdots & G'_{v1} \\ G'_{12} & G'_{22} & \cdots & G'_{v2} \\ \vdots & \vdots & \vdots & \vdots \\ G'_{1s} & G'_{2s} & \cdots & G'_{vs} \\ \vdots & \vdots & \vdots & \vdots \\ G'_{1N} & G'_{2N} & \cdots & G'_{vN} \end{bmatrix} \begin{bmatrix} G'_{21} & G'_{22} & \cdots & G'_{2s} & \cdots & G'_{2N} \\ 0 & 0 & \cdots & 0 & \cdots & 0 \\ G'_{41} & G'_{42} & \cdots & G'_{4s} & \cdots & G'_{4N} \\ 0 & 0 & \cdots & 0 & \cdots & 0 \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ G'_{v1} & G'_{v2} & \cdots & G'_{vs} & \cdots & G'_{vN} \\ 0 & 0 & \cdots & 0 & \cdots & 0 \end{bmatrix}. \tag{B.4}$$

Using Einstein notation, this equals

$$\begin{bmatrix} G'_{2i-1,1} G'^{2i,1} & G'_{2i-1,1} G'^{2i,2} & \cdots & G'_{2i-1,1} G'^{2i,s} & \cdots & G'_{2i-1,1} G'^{2i,N} \\ G'_{2i-1,2} G'^{2i,1} & G'_{2i-1,2} G'^{2i,2} & \cdots & G'_{2i-1,2} G'^{2i,s} & \cdots & G'_{2i-1,2} G'^{2i,N} \\ \vdots & \vdots & \vdots & \vdots & \vdots & \vdots \\ G'_{2i-1,N} G'^{2i,1} & G'_{2i-1,N} G'^{2i,2} & \cdots & G'_{2i-1,N} G'^{2i,s} & \cdots & G'_{2i-1,N} G'^{2i,N} \end{bmatrix}. \tag{B.5}$$

Keep in mind that, if we were to delete an edge from G , this would correspond to losing a column from CH , which would correspond to losing, say, the s th column from matrix (B.5). Taking the lower triangular portion of matrix (B.5) and calculating, we find that the m th element of the vector $\alpha^{(G)}$ is

$$\begin{aligned} \alpha_m^{(G)} &= a_{m2}[a_{m1} G'_{2i-1,2} G'^{2i,1}] + a_{m3}[a_{m1} G'_{2i-1,3} G'^{2i,1} + a_{m2} G'_{2i-1,3} G'^{2i,2}] + \cdots, \\ &+ a_{mk}[a_{m1} G'_{2i-1,k} G'^{2i,1} + \cdots + a_{m(k-1)} G'_{2i-1,k} G'^{2i,k-1}] + \cdots, \\ &+ a_{mn}[a_{m1} G'_{2i-1,N} G'^{2i,1} + \cdots + a_{m(N-1)} G'_{2i-1,N} G'^{2i,N-1}], \end{aligned} \tag{B.6}$$

where a_{mi} is the i th element of the m th null vector of CH or the matrix element $A_{m,i}$.

Now, let

$$\xi_m^s = a_{m(s+1)} a_{ms} G'_{2i-1,s+1} G'^{2i,s} + a_{m(s+2)} a_{ms} G'_{2i-1,s+2} G'^{2i,s} + \cdots + a_{mN} a_{ms} G'_{2i-1,N} G'^{2i,s}. \tag{B.7}$$

This is the portion of $\alpha_m^{(G)}$ that would vanish if we were to omit the edge that corresponds to the s th column of the matrix (B.5). This means that, if we remove this edge, we will end up with the subgraph G' and we can write

$$\alpha_m^{(G)} = \alpha_m^{(G')} + \xi_m^s. \quad (\text{B.8})$$

This equation is saying that the m th entry of the right-hand side of

$$A^{(G)}w = \alpha^{(G)} \quad (\text{B.9})$$

is given by the m th entry of the right-hand side of

$$A^{(G')}w = \alpha^{(G')} \quad (\text{B.10})$$

(the corresponding system of equations for the graph G') plus the term ξ_m^s .

From the assumption that $G \in \Gamma_w$ and by construction, we have

$$\alpha^{(G)} = \begin{bmatrix} \alpha_1^{(G')} + \xi_1^s \\ \alpha_2^{(G')} + \xi_2^s \\ \vdots \\ \alpha_K^{(G')} + \xi_K^s \end{bmatrix} = \sum_i \delta_i c_i, \quad (\text{B.11})$$

where the δ_i are coefficients in GF(2) and the c_i are columns of $A^{(G)}$, i.e. $\text{Rank}[A^{(G)}|\alpha^{(G)}] = \text{Rank}[A^{(G)}]$. Thus we have

$$\alpha^{(G')} = \sum_i \delta_i c_i - \xi^s. \quad (\text{B.12})$$

How does the matrix A change as we go from $G \rightarrow G'$ by this edge deletion? If the edge is a *dangling edge*, i.e., not part of a cycle, then we lose a column (column s), but if the edge deletion causes the breaking of M cycles, then A will lose M rows (in addition to column s), as the rows encode the cycle structure of the graph. In this case, the dimension (or length) of $\alpha^{(G')}$ will be M less than the dimension of $\alpha^{(G)}$, and the c_i will also be shorter by M entries. We call these shorter c_i, c'_i . Further, and most importantly, ξ^s will vanish, as mentioned. After taking this into consideration, we now can conclude that

$$\alpha^{(G')} = \sum_{i \neq s} \delta_i c'_i, \quad (\text{B.13})$$

where $A^{(G')} = [c'_1 c'_2 \cdots c'_{N-1}]$. Thus,

$$\text{Rank}[A^{(G')}|\alpha^{(G')}] = \text{Rank}[A^{(G')}]. \quad (\text{B.14})$$

The proof for edge contractions is similar. The main difference is that an edge contraction does not cause the loss of a cycle except when the edge in question belongs to a cycle of length three. Thus, in general, the contraction case is simpler except when dealing with cycles of length three. In this case the proof carries over in the same way. \square

Thus the set of graphs Θ is downwardly closed.

Lemma 1. *The obstruction set for Θ is finite.*

Proof. The set of graphs Θ is *downwardly closed* by the above lemma. One may then apply the Robertson–Seymour theorem (theorem 3) and immediately conclude that the number of forbidden minors of Θ is finite. \square

Lemma 6. A quadratic form $x^t Ax$ over $\text{GF}(2)$ is linear in x (equal to $x^t \text{diag}(A)$) iff A is symmetric.

Proof. Let x be an m -dimensional column vector and A an $m \times m$ matrix, both over $\text{GF}(2)$. Consider the quadratic form $x^t Ax = \sum_{ij} x_i A_{ij} x_j$ and assume that A is symmetric: $A = A^t$. Then

$$\begin{aligned} x^t Ax &= \sum_{i < j} A_{ij} x_i x_j + \sum_i A_{ii} x_i^2 + \sum_{i > j} A_{ij} x_i x_j \\ &= \sum_{i < j} A_{ij} x_i x_j + \sum_i A_{ii} x_i + \sum_{j > i} A_{ij} x_j x_i, \end{aligned} \quad (\text{B.15})$$

where in the second line we used $x_i^2 = x_i$ (true over $\text{GF}(2)$), exchanged i and j in the third summand and used $A_{ij} = A_{ji}$. The first and third summands are equal and hence add up to zero over $\text{GF}(2)$. We are left with the second, linear term, i.e. $x^t Ax = x^t \text{diag}(A)$, where $\text{diag}(A)$ denotes a vector comprising the diagonal of A .

Next, assume that A is not symmetric. Then there exists a pair of indices $i' < j'$ such that $A_{i'j'} \neq A_{j'i'}$, i.e. $A_{i'j'} + A_{j'i'} = 1$. As above, we have: $x^t Ax = \sum_{i < j} A_{ij} x_i x_j + \sum_i A_{ii} x_i + \sum_{j > i} A_{ji} x_j x_i$. Consider the index pair (i', j') in this sum:

$$A_{i'j'} x_{i'} x_{j'} + A_{j'i'} x_{j'} x_{i'} = (A_{i'j'} + A_{j'i'}) x_{i'} x_{j'} = x_{i'} x_{j'}. \quad (\text{B.16})$$

Thus the quadratic form contains at least one nonlinear (quadratic) term $x_{i'} x_{j'}$. \square

Appendix C. Algorithm for minor testing

Note that all calculations are done modulo 2.

Input: A graph G for which we wish to determine if there exists some satisfying edge interaction w that satisfies equation (34). Specifically, we considered $G = K_{3,3}, K_5, \bar{K}_{3,3}$ ($K_{3,3}$ with one edge deleted) and K_4 .

Output: A binary vector w that is a satisfying bond distribution or a null vector (indicating no such bond distribution).

1. From the incidence matrix A of G , obtain the following items:
 - (a) All vectors a_i belonging to the null space \mathcal{L} of the incidence matrix. These row vectors form a matrix M .
 - (b) Construct a matrix representation H of the possible corresponding quantum circuits (under the mapping presented earlier). From H , construct $Q = \text{lwtr } H^t C H$. This matrix will have variables z_i corresponding to all the possible ways that one can include or omit Z operations (changing these affects the types of edge interactions that one obtains, if any).
2. Form the vector B whose i th entry is $B_i = a_i^t Q a_i$, where a_i are the elements of the null space of \mathcal{L} . This is the left-hand side of equation (34). Each entry of B consists of linear equations whose variables z_i represent the presence or absence of a Z operation in a quantum circuit that corresponds to G .
3. Form a matrix W whose rows are all possible bonds.

4. Produce a matrix D whose i th row D_i is equal to MW_i . These are all possible values of the right-hand side of equation (34). (Note that, due to symmetry, there will be many repeats, so that the total number of possible bonds to check is far fewer than all possible bonds.)
5. Attempt to solve the system of linear equations $B_k = D_{1,k}, B_k = D_{2,k}, \dots, B_k = D_{|L|,k}$ over $\text{GF}(2)$ (where k runs from 1 to the number of rows of D) for the variables z_i . A solution for some k gives information for a specific circuit representation H . If no solution for the z_i exists, then there is no satisfying bond distribution w for equation (34). This is indeed the case for $K_{3,3}, K_5, \bar{K}_{3,3}$ and K_4 . If there is a solution for some fixed k , continue.
6. Take this specific H (i.e. this H has no variables and corresponds to a specific circuit given by the solution of the z_i above) and now form $B_i = a_i^\dagger(\text{lwtr } H^t C H)a_i$ again. This time, however, B contains no variables and is a numerical vector. Thus, one now has the binary vector B and the matrix M .
7. Solve the linear system $Mw = B$ and output the edge interaction w .

Having applied this algorithm, we proved, using mathematical software, that the obstruction set for Θ includes $K_{3,3}, K_5, \bar{K}_{3,3}$ and K_4 , i.e. lemma 2.

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